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Application#	Patent#	Status	Date Filed	Title	Inventor Name
10190097	6806008	150	07/03/2002	METHOD FOR ADJUSTING A TEMPERATURE IN A RESIST PROCESS	SEIDEL, TORSTEN
10435449	6887722	150	05/09/2003	METHOD FOR EXPOSING A SEMICONDUCTOR WAFER	SEIDEL, TORSTEN
10715073	Not Issued	71	11/17/2003	Configuration and method for detecting defects on a substrate in a processing tool	SEIDEL, TORSTEN

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